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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/774,489	02/10/2004	Hiroshi Sotozaki	2004_0208	8916
513 75	590 02/07/2006	•	EXAM	INER
WENDEROT	H, LIND & PONAC	MACARTHUR, SYLVIA		
2033 K STREET N. W. SUITE 800 WASHINGTON, DC 20006-1021			ART UNIT	PAPER NUMBER
			1763	

DATE MAILED: 02/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			0		
		Application No.	Applicant(s)		
Office Action Summary		10/774,489	SOTOZAKI ET AL.		
		Examiner	Art Unit		
		Sylvia R. MacArthur	1763		
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the	e correspondence address		
WHIC - Exte after - If NC - Failu Any	IORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATES OF THE MAILING T	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDO	ON. timely filed om the mailing date of this communication. NED (35 U.S.C. § 133).		
Status			•		
1)⊠	Responsive to communication(s) filed on 26 Ja	anuary 2006.			
2a)⊠	This action is FINAL . 2b)⊠ This	action is non-final.			
3)[Since this application is in condition for allowar	nce except for formal matters, p	prosecution as to the merits is		
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11,	453 O.G. 213.		
Disposit	ion of Claims				
4)🖂	Claim(s) 1,10-15 and 22-34 is/are pending in the	he application.			
	4a) Of the above claim(s) is/are withdraw	wn from consideration.			
5)	Claim(s) is/are allowed.				
6)⊠	Claim(s) <u>1,10-15 and 22-34</u> is/are rejected.				
7)	Claim(s) is/are objected to.				
8)[Claim(s) are subject to restriction and/or	r election requirement.			
Applicati	ion Papers				
9)[The specification is objected to by the Examine	r.			
10)⊠	The drawing(s) filed on 10 February 2004 is/are	e: a)⊠ accepted or b)□ objec	ted to by the Examiner.		
	Applicant may not request that any objection to the	drawing(s) be held in abeyance. S	see 37 CFR 1.85(a).		
	Replacement drawing sheet(s) including the correct	ion is required if the drawing(s) is o	objected to. See 37 CFR 1.121(d).		
11)	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	ce Action or form PTO-152.		
Priority ι	under 35 U.S.C. § 119				
_	Acknowledgment is made of a claim for foreign ☐ All b)☐ Some * c)☐ None of:	priority under 35 U.S.C. § 119((a)-(d) or (f).		
	1. Certified copies of the priority documents have been received.				
	2. Certified copies of the priority documents	s have been received in Applica	ation No		
	3. Copies of the certified copies of the prior	ity documents have been recei	ved in this National Stage		
	application from the International Bureau	, , , ,			
* \$	See the attached detailed Office action for a list of	of the certified copies not receive	ved.		
Attachmen	• •				
	e of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948)	4) LInterview Summa Paper No(s)/Mail			
3) 🔲 Inform	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) or No(s)/Mail Date		Patent Application (PTO-152)		

U.S. Patent and Trademark Office PTOL-326 (Rev. 7-05)

DETAILED ACTION

RCE

1. The request filed on 1/26/2006 for an RCE is acceptable and an RCE has been established. An action on the RCE follows.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1, 10-15, and 22-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Togawa (EP 0761387) in view of Kojima et al (US 6,989,228).

Togawa teaches a polishing apparatus and a method of polishing and then cleaning substrates comprising at least two polishing units/section (1a,1b) and cleaning units (7a,7b,8a,8b). The pairs of cleaning units are disposed on one side of the transfer line. Cleaning units 8a,8b also serves as a drying unit, according page 4 lines 42-49. According to the cleaning units 7a,7b perform the same cleaning function see page 4, lines 47-49. The first cleaning unit cleans one substrate at a time as described in page 5 lines 41-52. Transfer robots 4a, 4b are used as conveyor to the substrate throughout the polishing apparatus. Col. 5liens 25-30 teaches that cleaning units 7a,7b are primary cleaning units whiles 8a,8b are secondary cleaning units.

Togowa et al fails to teach a) cleaning while the substrate is supported on a table and b) a step wherein the first and second substrate are cleaned in the same chamber.

Kojima et al teaches a method and apparatus for processing samples wherein a wafer is cleaned in wet cleaning chamber 30 and the substrate is supported on a table and rotated.

The motivation to modify the method of Togowa to include supporting wafer on a table is to specify the type of cleaning used and provide a means of supporting the wafer during cleaning. Thus, it would have been obvious for one of ordinary skill in the art at the time of the claimed invention to modify the method of Togowa to use a table to support the wafer.

Regarding the use of a common cleaning chamber:

Neither Togowa et al nor Kojima et al specifically teach a common cleaning chamber. The omission of one of the secondary cleaning units of Togowa would have been an obvious modification in the parallel processing steps of Togowa since there are two secondary cleaning units that perform the same cleaning function. According to In re Wilson 153 USPQ 740, the omission of an element with the consequent loss of its function is obvious. The consequence of omitting one of the secondary cleaning units of Togowa would be that that the primary cleaning steps would occur simultaneously while the secondary step would occur consecutively. Thus, it would have been obvious for one of ordinary skill in the art at the time of the claimed invention to omit one of the secondary cleaning units of Togowa per In re Wilson.

Claims 11 and 12 of Togawa cites that parallel processing is performed to increase throughput, such that a workpiece is polished by a polishing unit /cleaning unit pair on one side of the apparatus as another workpiece is processed by the same type of pair on the opposite side of the transfer line.

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Regarding claims 14 and 31: Togawa fails to specify that the cleaning unit supplies an etching liquid. Nevertheless, Togawa does cite in page 2 line 29 that the wafer is cleaned by water.

4. Claims 1, 10-15, and 22-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Katsuoka et al (US 6,918,814) in view of Kojima et al (US 6,989,228).

Katsuoka et al teaches a pair of polishing units 10a,10b, a pair of primary cleaning units 18a,b, and secondary cleaning units of 14a,b. Col7 line 22 Katsuoka et al teaches that the polishing apparatus works in a parallel fashion, see col. 7 lines 34-43.

Katsuoka et al in col.6 lines 47-50 teaches that the cleaning units are selected to suit applications and fails to teach the specific type of cleaning apparatus performing the cleaning steps.

Katsuoka et al fails to teach a) cleaning while the substrate is supported on a table and b) a step wherein the first and second substrate are cleaned in the same chamber.

Kojima et al teaches a method and apparatus for processing samples wherein a wafer is cleaned in wet cleaning chamber 30 and the substrate is supported on a table and rotated.

The motivation to modify the method of Katsuoka to include supporting wafer on a table is to specify the type of cleaning used and provide a means of supporting the wafer during cleaning. Thus, it would have been obvious for one of ordinary skill in the art at the time of the claimed invention to modify the method of Katsuoka to use a table to support the wafer.

Regarding the use of a common cleaning chamber:

Katsuoka et al teaches parallel processing of the workpieces in col. 7 lines 22-46.

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Neither Katsuoka et al nor Kojima et al specifically teach a common cleaning chamber. The omission of one of the secondary cleaning units of Katsuoka would have been an obvious modification in the parallel processing steps of Katsuoka since there are two secondary cleaning units that perform the same cleaning function. According to In re Wilson 153 USPQ 740, the omission of an element with the consequent loss of its function is obvious. The consequence of omitting one of the secondary cleaning units of Katsuoka would be that that the primary cleaning steps would occur simultaneously while the secondary step would occur consecutively. Thus, it would have been obvious for one of ordinary skill in the art at the time of the claimed invention to omit one of the secondary cleaning units of Katsuoka per In re Wilson.

Regarding claims 14 and 31: Katsuoka et al fails to specify that the cleaning unit supplies an etching liquid. Nevertheless, Katsuoka et al does cite in col. 7 lines 45-58 that the wafer is cleaned by water.

Response to Arguments

5. Applicant's arguments with respect to claims 1 and 10-15 have been considered but are moot in view of the new ground(s) of rejection.

The prior art by Kojima et al teaches a specific type of cleaning process wherein a table is used to support and rotate the wafer while it is cleaned. In re Wilson held that it is obvious to omit an element in this case one of the secondary cleaning units with consequent loss of its function (increase throughput due to a need to wait for the processing of each workpiece in the common cleaning unit).

Conclusion

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sylvia R. MacArthur whose telephone number is 571-272-1438. The examiner can normally be reached on M-F during the hours of 8:30 a.m. and 5 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Parviz Hassanzadeh can be reached on 571-272-1435. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Sylyja R MacArti Patent Examiner Art Unit 1763

February 4, 2006